

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	95	714/30.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 08:29
L3	25	714/33.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 08:40
L4	92	714/37.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 08:43
L5	179	714/38.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 08:52
L6	59	714/39.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 08:56
L7	86	714/45.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:03
L8	146	714/47.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:09
L9	13	714/51.ccls. and @pd>="20081020"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:13
L10	84	"code coverage" and multiplex\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:18

L12	5133	"address multiplexer"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:40
L13	162	"address multiplexer" with (input and "address counter")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:41
L14	1	("address multiplexer" with (input and "address counter")) and "code coverage"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2009/06/18 09:42
S1	44	(GUPTA-SHIV-KUMAR GUPTA-SHIV-K ILPAKURTY ILPAKURTY-R ILPAKURTY- RAVI \$-narendranath).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 08:43
S3	6	714/33.ccls. and "code coverage"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:32
S4	72	714/33.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:32
S5	8	714/33.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:36
S8	6	714/30.ccls. and "code coverage"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 12:54
S9	6	714/30.ccls. and ("code coverage" "statement coverage" "condition\$2 coverage" "path coverage")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:43
S10	23	714/30.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:44

S12	41	714/37.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:52
S13	7	714/37.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:44
S14	12	714/38.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:20
S17	72	714/38.ccls. and ("code coverage" "statement coverage" "condition\$2 coverage" "path coverage")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 09:52
S19	43	714/39.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:20
S20	6	714/39.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:26
S22	35	714/45.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:26
S23	4	714/45.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:29
S25	32	714/47.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:30
S26	13	714/47.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:30

S28	7	714/51.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:30
S29	0	714/51.ccls. and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:30
S30	3	("code coverage" "statement coverage" "condition\$2 coverage" "path coverage") and (firmware with test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:30
S31	4	"code coverage" with firmware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:56
S33	35	"code coverage" and "address bus"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 10:56
S34	898	714/30.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S35	483	714/33.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S36	1827	714/38.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S37	153	714/30.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S38	508	714/37.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19

S39	505	714/39.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S40	142	714/51.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S41	199	714/38.ccls. and coverage	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S42	497	714/45.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S43	1140	714/47.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S44	434	"code coverage"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:19
S45	4	("code coverage" "statement coverage" "condition\$2 coverage" "path coverage") with firmware	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 12:30
S46	72	714/30.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:14
S47	18	714/33.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:30
S48	23	714/37.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:31

S49	118	714/38.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:41
S50	29	714/39.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:42
S51	34	714/45.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:48
S52	71	714/47.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:53
S53	8	714/51.ccls. and @pd>="20061210"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 13:53
S54	282	"code coverage" and "memory" and (increment\$3 or count\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 14:03
S55	77	"code coverage" and ("memory" with (increment \$3 or count\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/05/19 14:03
S56	53	714/30.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/07 14:22
S57	27	714/33.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/07 14:24
S58	40	714/37.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 08:30

S59	88	714/38.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 09:20
S60	37	714/39.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 09:28
S61	36	714/45.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 09:43
S62	72	714/47.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 09:48
S63	9	714/51.ccls. and @pd>="20070519"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/11/08 09:48
S64	58	714/30.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 13:59
S65	8	714/33.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:04
S66	26	714/37.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:04
S67	64	714/38.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:07
S68	21	714/39.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:14

S69	39	714/45.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:15
S70	52	714/47.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:38
S71	8	714/51.ccls. and @pd>="20071108"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 14:57
S72	74	"code coverage" and multiplex\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 15:36
S73	0	"code coverage memory" with multiplex\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/21 10:37
S74	0	trac\$3 and ("coverage memory" with multiplex\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/21 10:38
S75	73	714/30.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/16 13:29
S76	24	714/33.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 10:15
S77	64	714/37.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 10:34
S78	134	714/38.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 10:49

S79	38	714/39.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 11:05
S80	62	714/45.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 12:49
S81	104	714/47.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 12:53
S82	9	714/51.ccls. and @pd>="20080320"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/10/20 13:35

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